

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: M. NAGASU et al.
Serial No.: (New continuation application under 37 CFR §1.53[b]
of application U.S. Appln. Serial No. 10/135,530)
Filed: September 24, 2003
For: SEMICONDUCTOR CIRCUIT, METHOD OF DRIVING
THE SAME AND SEMICONDUCTOR DEVICE
Group: 2816
Examiner: K. B. WELLS

CLAIM FOR PRIORITY

MS: PATENT APPLICATIONS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sept. 24, 2003

Sir:

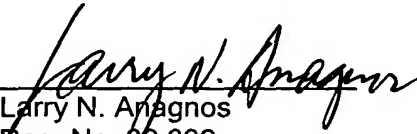
Pursuant to 35 U.S.C. §119 and 37 CFR §1.55, applicants hereby claim the right
of priority based on the following foreign applications filed in Japan:

Application No. 7-241233, filed September 20, 1995

Application No. 7-122117, filed May 22, 1995

Certified copies of the foreign applications were filed on May 21, 1996 in prior
U. S. application Serial No. 08/651,875, filed on May 21, 1996.

Respectfully submitted,
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